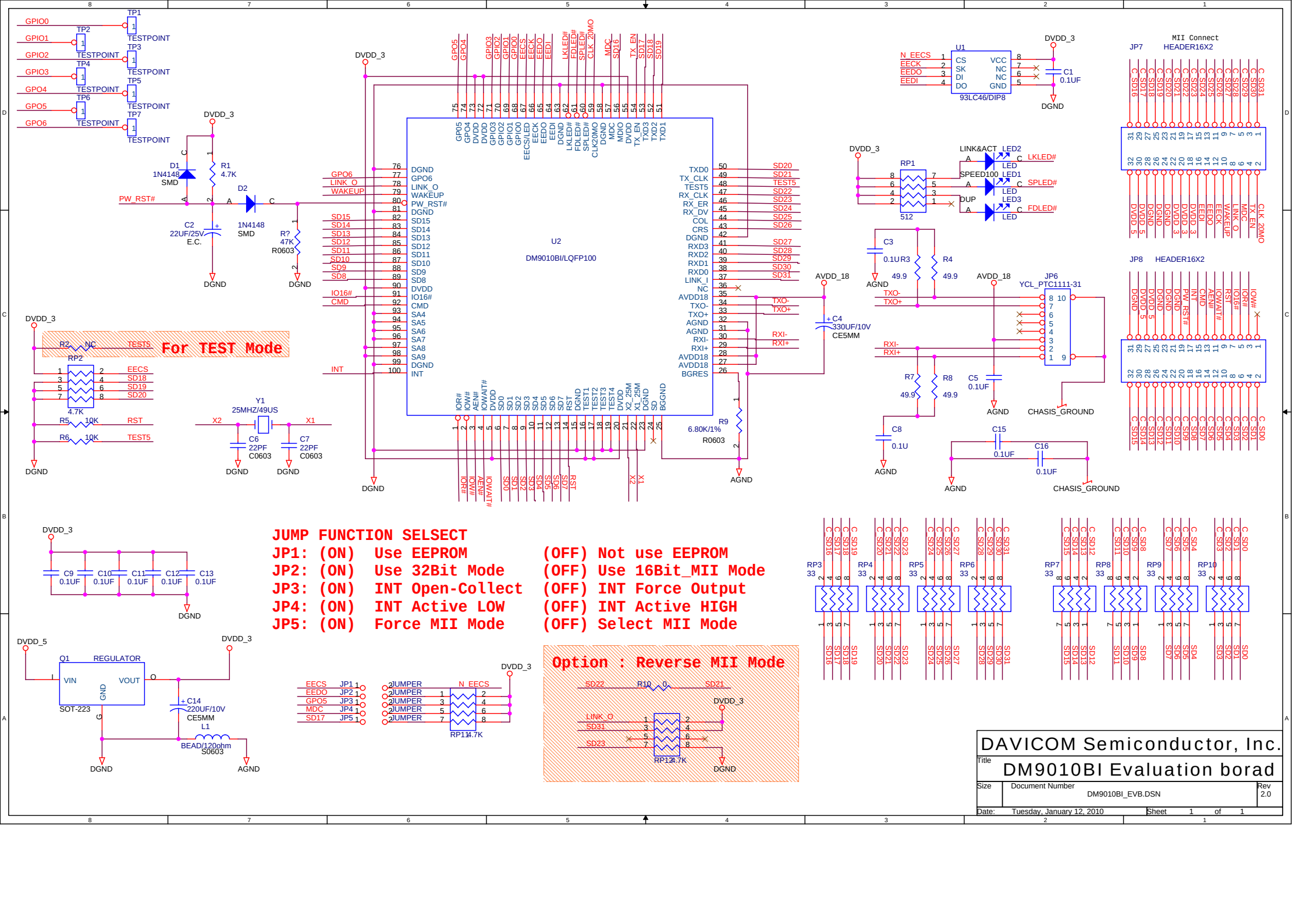
GPIO0
GPIO1
GPIO2
GPIO3
GPO4
GPO5
GPO6
TESTPOINT
DVDD_3
DGND
AGND
DVDD_5
PW_RST#
D1
1N4148
SMD
R1
4.7K
D2
C2
22UF/25V
E.C.
R?
47K
R0603
U2
DM9010BI/LQFP100
GPO6
LINK_O
WAKEUP
PW_RST#
SD15
SD14
SD13
SD12
SD11
SD10
SD9
SD8
IO16#
CMD
INT
TXO-
TXO+
RXI-
RXI+
AVDD_18
BGRES
R9
6.80K/1%
R0603
U1
93LC46/DIP8
N_EECS
EECK
EEDO
EEDI
C1
0.1UF
JP7
HEADER16X2
MII Connect
JP8
HEADER16X2
RP1
512
LINK&ACT LED2
SPEED100 LED1
DUP LED3
LKLED#
SPLED#
FDLED#
C3
0.1U
R3
49.9
R4
49.9
JP6
YCL_PTC1111-31
R7
49.9
R8
49.9
C5
0.1UF
C8
0.1U
C15
0.1UF
C16
0.1UF
CHASIS_GROUND
For TEST Mode
R2
NC
TEST5
RP2
4.7K
EECS
SD18
SD19
SD20
R5
10K
RST
R6
10K
TEST5
Y1
25MHZ/49US
X2
X1
C6
22PF
C0603
C7
22PF
C0603
C4
330UF/10V
CE5MM
C9
0.1UF
C10
0.1UF
C11
0.1UF
C12
0.1UF
C13
0.1UF
Q1
REGULATOR
VIN
VOUT
GND
SOT-223
C14
220UF/10V
CE5MM
L1
BEAD/120ohm
S0603
EECS JP1
EEDO JP2
GPO5 JP3
MDC JP4
SD17 JP5
JUMPER
N_EECS
RP11
4.7K
RP3
RP4
RP5
RP6
RP7
RP8
RP9
RP10
33
JUMP FUNCTION SELSECT
JP1: (ON) Use EEPROM (OFF) Not use EEPROM
JP2: (ON) Use 32Bit Mode (OFF) Use 16Bit_MII Mode
JP3: (ON) INT Open-Collect (OFF) INT Force Output
JP4: (ON) INT Active LOW (OFF) INT Active HIGH
JP5: (ON) Force MII Mode (OFF) Select MII Mode
Option : Reverse MII Mode
SD22
R10
0
SD21
LINK_O
SD31
SD23
RP12
4.7K
DAVICOM Semiconductor, Inc.
Title
DM9010BI Evaluation borad
Size
Document Number
DM9010BI_EVB.DSN
Rev
2.0
Date: Tuesday, January 12, 2010
Sheet 1 of 1