



聯傑國際股份有限公司

DAVICOM Semiconductor , Inc.

DM9000 Errata

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Reason:

Because the pin IOR# used to read the DM9000 internal register is unqualified that means even the IOR command is not intend to access the DM9000, the internal register data bus will be filled with read data when the IOR# is active.

When the IOW command to write data to the DM9000 internal register, the data is written within the IOW# deasserted 42ns.

If there is an IOR# asserted within 42ns after IOW command deasserted, the data to be written to register will be conflicted in internal register data bus. This will cause the data written to the intend register with error value.

Correction in application:

Use OR function to gate the pin IOR# with the chip_select (decode the IO base of the DM9000).

Logic function:

$\text{new_IOR\#} = \text{old_IOR\#} + \text{chip_select}$.

Where “+” is an OR function, and if both the IOR# and the chip_select are active low.