

Data sheet 修改記錄單

Part No :	DM9013
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版 次	標準書修改記錄	日期	修改者
P02	<ol style="list-style-type: none"> 1. Modify 5.10.2 Strap pin in 2-port mode, P59 TXD2_2 description.(P15) 2. Add 6. Control and Status Register Set, Register Wake Up Control Register (0FH), and TX Block Size Control Register (3FH).(P18) 3. Modify 6.10 EPROM & PHY Data Register (0DH~0EH) description.(P19). 4. Add 6.19 TX/RX Memory Size Control Register (3FH) description. (P21) 5. Add 6.41 Memory Data Read Command with Address Increment Register (F2H) And 6.42 Memory Data Read address Register (F4H) description. (P27) 6. Modify 7.EEPROM Format, word: 18 to reserved.(P36) 7. Modify 7.EEPROM Format, word: 45, 46, 47: C10H~C15H to CAH~CFH. (P37) 8. Modify 8.5 Auto-negotiation Advertisement Register (ANAR) – 04H, bit 10 Flow Control Support Default to 1.(P42) 9. Add 8.9 DAVICOM Specified Configuration and Status Register (DSCSR) – 11H, ANMB[3:0] Bit: 1000, Auto-negotiation completed successfully. (P46) 10. Modify 9.2.7 and 9.2.8. to 9.2.7 Full Duplex Flow Control and 9.2.8 Half Duplex Flow Control.(P51) 11. Modify 9.2.13.1 Port-Based VLAN (5). Set register B1h to 0x02h. (6). Set register B2h to 0x05h. (7). Set register B3h to 0x0Ah. (8). Set register B4h to 0x04h.(P55) 12. Add 10.2 Operating Conditions, 2-port status of PD (Power Dissipation). (P61) 13. Add 10.3 DC Electrical Characteristics, 100TX+/- Differential Output Current and 10TX+/- Differential Output Current, change to 19, 20, 21 mA, and 44, 50, 56 mA. (P62) 14. Add 10.4.1 Power On Reset Timing. (P62) 15. Modify 10.4.2 Processor I/O Read Timing, Symbol Parameter.(P63) 16. Modify 10.4.7 EEPROM Timing T4 unit.(P66) 17. Add 11. Application circuit. (P67) 18. Add 13.Ordering Information: MAIL: sales@davicom.com.tw and 	122807	蔡長勳



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