



**DM9161A**

*10/100 Mbps Fast Ethernet Physical Layer Single Chip Transceiver*

**DAVICOM Semiconductor, Inc.**

**DM9161A**

**10/100 Mbps Fast Ethernet  
Physical Layer Single Chip Transceiver**

**Engineer Change Notes**

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JUL 13 2006  
Revision 1: DM9161A-17-ECN-P01  
BY: Dec. 22, 2005

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BY: *mc*



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10/100 Mbps Fast Ethernet Physical Layer Single Chip Transceiver

## The differences between DM9161A Revision 6 and Revision 7

Function	Revision 6	Revision 7
AUTO-MDIX function	Not support AUTO-MDIX function in non Auto-negotiation mode.	Support AUTO-MDIX function in non Auto-negotiation mode

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Revision 7, DM9161A-17-ECN P01

Dec. 22, 2005

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