Note: DVDD is 3.3V.

Note: R11, C11 is for power on reset. It can also be reset by Host Processor.

Note4: R11, C11 is for power on reset. It can also be reset by Host Processor.

Note2: R9 pull high, LED mode is mode 1; otherwise it is mode 0.

Note1: R10 pull high, 8-bit mode.

For Application Reference Design Only

IO base address = Host_nCS
IO data address = Host_nCS + 0x04 (if Host_SA2 connect to CMD (92pin))

Note: DM9000 Host Parallel Bus, for 8bits
For Application Reference Design Only