Note: DVDD is 3.3V.

Note: DM9000 ISA Bus, for 8/16 bits

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Note: 8/16 bit selection; removed R10 when using 16-bit mode; otherwise it is 8-bit mode.

Note2: TXD[2:0] is used the strap pins of IO base address. Default is 0x300h.

Note3: TXD[2:0] can be decoded from 300H to 370H.

For example:

IO base address should be matched with TXD[2:0]:

A9 A8 A7 A6 A5 A4 A3 A2 A1 A0
1 1 1 1 0 0 0 0 1 1 (310H)
1 0 0 0 0 1 1 1 1 1 (330H)

IO data address = IO base address + 0x04 (SA2 connect to CMD (92pin))

Note: DM9000 ISA Bus, for 8/16bits

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