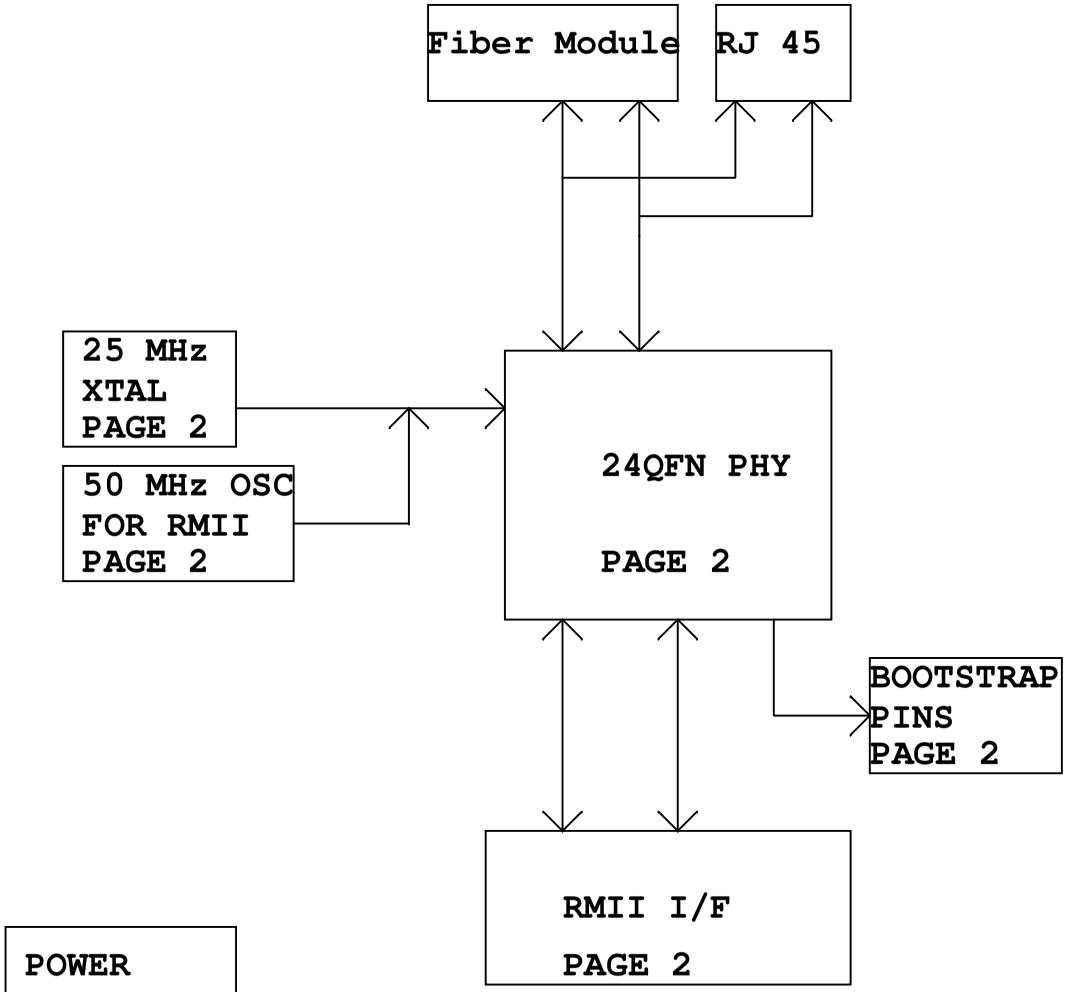
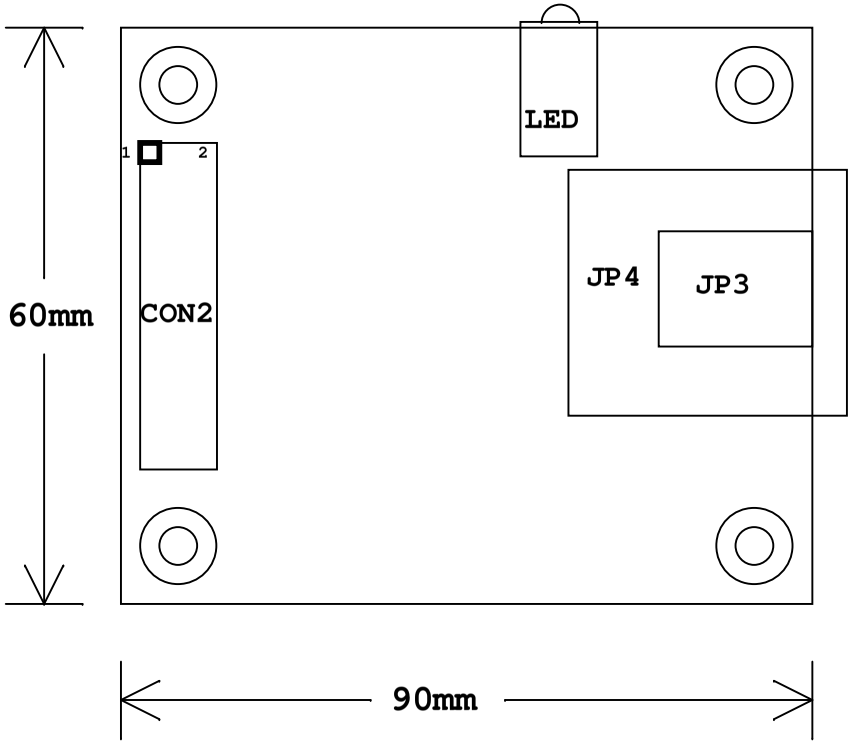
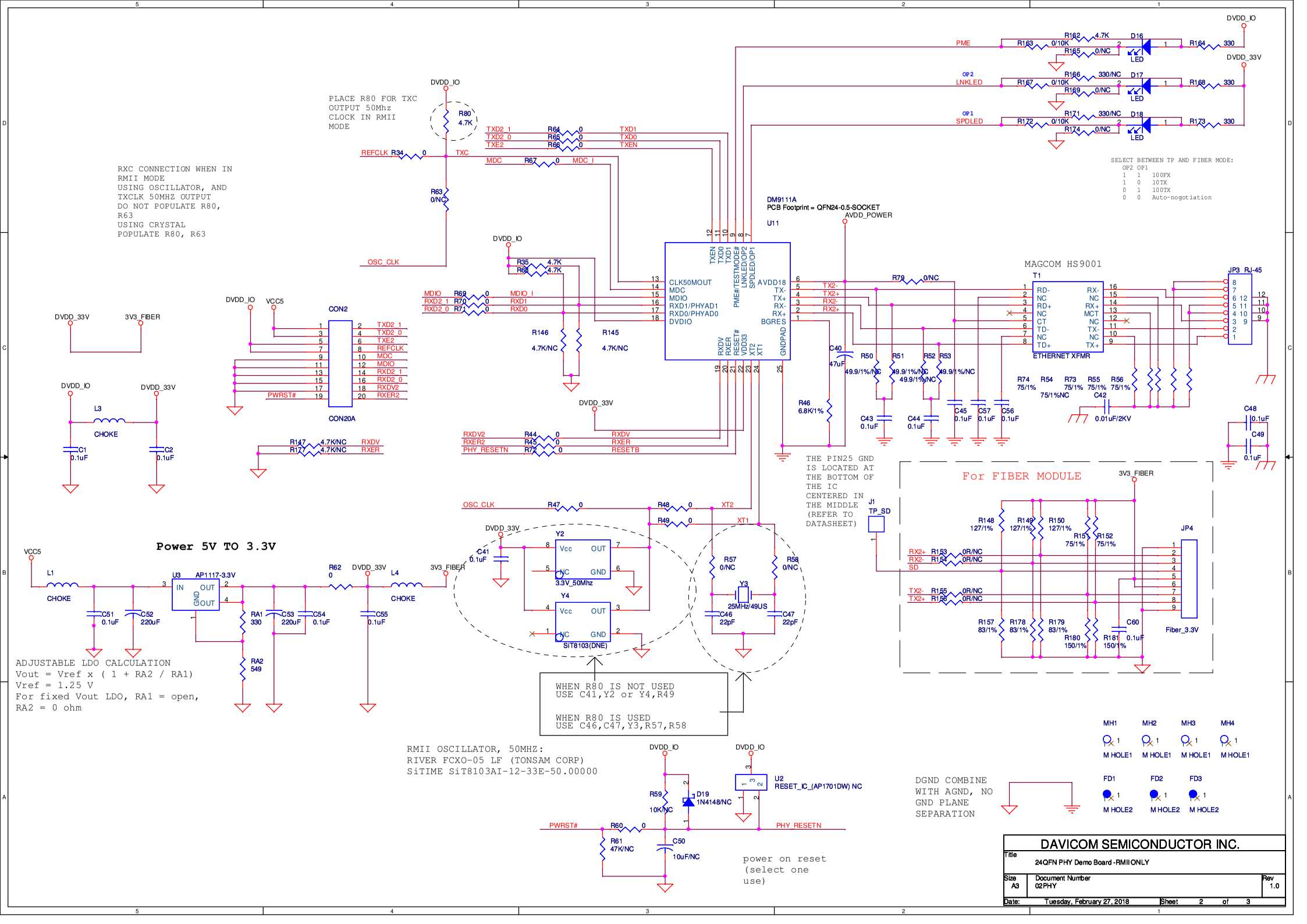




Davicom Semiconductor Inc.		
Title: 24QFN PHY Demo Board (PCB_Overview)		
Size: A4	Document Number: 01TOP	Rev: 1.0
Date: Friday, February 23, 2018	Sheet 1 of 3	


PLACE R80 FOR TXC OUTPUT 50Mhz CLOCK IN RMII MODE
RXC CONNECTION WHEN IN RMII MODE USING OSCILLATOR, AND TXCLK 50MHZ OUTPUT DO NOT POPULATE R80, R63
USING CRYSTAL POPULATE R80, R63
DM9111A
PCB Footprint = QFN24-0.5-SOCKET
U11
AVDD_POWER
SELECT BETWEEN TP AND FIBER MODE:
OP2 OP1
1 1 100FX
1 0 10TX
0 1 100TX
0 0 Auto-nogotiation
MAGCOM HS9001
T1
ETHERNET XFMR
JP3_RJ-45
CON2
CON20A
THE PIN25 GND IS LOCATED AT THE BOTTOM OF THE IC CENTERED IN THE MIDDLE (REFER TO DATASHEET)
For FIBER MODULE
Power 5V TO 3.3V
ADJUSTABLE LDO CALCULATION
Vout = Vref x (1 + RA2 / RA1)
Vref = 1.25 V
For fixed Vout LDO, RA1 = open,
RA2 = 0 ohm
WHEN R80 IS NOT USED
USE C41,Y2 or Y4,R49
WHEN R80 IS USED
USE C46,C47,Y3,R57,R58
RMII OSCILLATOR, 50MHZ:
RIVER FCXO-05 LF (TONSAM CORP)
SiTIME SiT8103AI-12-33E-50.00000
power on reset (select one use)
DGND COMBINE WITH AGND, NO GND PLANE SEPARATION
DAVICOM SEMICONDUCTOR INC.
Title 24QFN PHY Demo Board -RMII ONLY
Size A3
Document Number 02PHY
Rev 1.0
Date: Tuesday, February 27, 2018
Sheet 2 of 3

VER	DATE	ENGINEER	NOTE
1.0	2017/12/12	WILLIE NIOU Alliang	INITAL CIRCUIT CREATION

Davicom Semiconductor Inc.

Title: 24QFN PHY Demo Board		
Size: A4	Document Number: HISTORY	Rev: 1.0
Date: Tuesday, February 27, 2018	Sheet 3 of 3	